

## Freeform Search

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<b>Database:</b>	<div style="border: 1px solid black; padding: 2px;">         US Pre-Grant Publication Full-Text Database          US Patents Full-Text Database          US OCR Full-Text Database          EPO Abstracts Database          JPO Abstracts Database          Derwent World Patents Index          IBM Technical Disclosure Bulletins       </div>
<b>Term:</b>	<div style="border: 1px solid black; padding: 2px;">         15 and 14 and 16 and 17 and 18 and 19 and 110       </div>
<b>Display:</b>	<div style="border: 1px solid black; padding: 2px;">50</div> Documents in <b>Display Format:</b> <div style="border: 1px solid black; padding: 2px;">-</div> Starting with Number <div style="border: 1px solid black; padding: 2px;">1</div>
<b>Generate:</b> <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	

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Search

Clear

Interrupt

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### Search History

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DATE: Tuesday, September 14, 2004   [Printable Copy](#)   [Create Case](#)

#### Set Name Query

side by side

#### Hit Count Set Name

result set

*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR*

<u>L13</u>	15 and 14 and 16 and 17 and 18 and 19 and 110	30	<u>L13</u>
<u>L12</u>	15 and 14 and 16 and 17 and 18 and 19 and 110	30	<u>L12</u>
<u>L11</u>	15 same 14 same 16 same 17 same 18 same 19 same 110	0	<u>L11</u>
<u>L10</u>	mos	771058	<u>L10</u>
<u>L9</u>	threshold\$3	531357	<u>L9</u>
<u>L8</u>	modulat\$3	657755	<u>L8</u>
<u>L7</u>	transistor\$1	718033	<u>L7</u>
<u>L6</u>	((constant adj current) adj sourc\$1)	28241	<u>L6</u>
<u>L5</u>	feedback or (fed adj back) or (feed\$3 adj back)	490195	<u>L5</u>
<u>L4</u>	(compensat\$3 adj circuit)	31328	<u>L4</u>
<u>L3</u>	L2 same modulat\$3	18	<u>L3</u>
<u>L2</u>	L1 same (feedback or (feed\$3 adj back) or (fed adj back))	334	<u>L2</u>
<u>L1</u>	transistor\$1 with compensat\$3 with current with source	2462	<u>L1</u>

END OF SEARCH HISTORY

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<b>Term:</b>	<div style="border: 1px solid black; padding: 2px;">         l24 and (compensat\$ adj circuit)       </div>
<b>Display:</b>	<div style="border: 1px solid black; padding: 2px;">50</div>
<b>Documents in Display Format:</b>	<div style="border: 1px solid black; padding: 2px;">-</div>
<b>Starting with Number</b>	<div style="border: 1px solid black; padding: 2px;">1</div>
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result set

*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR*

<u>L25</u>	l24 and (compensat\$ adj circuit)	11	<u>L25</u>
<u>L24</u>	L22 and l21	63	<u>L24</u>
<u>L23</u>	L22 and l21	63	<u>L23</u>
<u>L22</u>	341/\$.ccls. or 326/\$.ccls. or 327/\$.ccls.	154329	<u>L22</u>
<u>L21</u>	L20 and (l5 with l6) and l3 and l7	185	<u>L21</u>
<u>L20</u>	l10 same l1	2237	<u>L20</u>
<u>L19</u>	L18 same l4 same mos	0	<u>L19</u>
<u>L18</u>	l1 same l7 same l5 same l6 same l2 same l3	54	<u>L18</u>
<u>L17</u>	(L1 same l7) and (l5 with l6) and l2 and l3	1603	<u>L17</u>
<u>L16</u>	L15 and l1	650	<u>L16</u>
<u>L15</u>	(l5 with l6) and l10 and l2 and l3 and l7	1038	<u>L15</u>
<u>L14</u>	l11 and l7	3	<u>L14</u>
<u>L13</u>	l11 and l7	3	<u>L13</u>
<u>L12</u>	L11 same l7	0	<u>L12</u>
<u>L11</u>	L10 same l1 same (l5 with l6) same l2 same l3	4	<u>L11</u>
<u>L10</u>	(mos with l4)	90249	<u>L10</u>

<u>L9</u>	L8 same mos	0	<u>L9</u>
<u>L8</u>	l1 same l2 same l3 same l4 same l5 same l6 same l7	30	<u>L8</u>
<u>L7</u>	feedback or (feed\$3 adj back) or (fed adj back)	490195	<u>L7</u>
<u>L6</u>	sourc\$1	2973119	<u>L6</u>
<u>L5</u>	current	2688718	<u>L5</u>
<u>L4</u>	transistor\$1	718033	<u>L4</u>
<u>L3</u>	modulat\$3	657755	<u>L3</u>
<u>L2</u>	threshold\$3	531357	<u>L2</u>
<u>L1</u>	compensat\$	884673	<u>L1</u>

END OF SEARCH HISTORY